

10/7/3,441

Referring to FIG. 7, a more detailed diagram of analyzer 404 is shown. The analyzer 404 generally comprises a block (or circuit) 420, a block (or circuit) 422 and a block (or circuit) 424 ~~4242~~. The circuit 420 may be implemented as a 4-set detector. The circuit 422 may be implemented as a segment detector. The circuit 424 may be implemented as a controller. The controller 424 ~~422~~ bi-directionally communicates with the 4-set detector 420 and the segment detector 422 through a bus 430a and a bus 430b. The 4-set detector 420 has a number of outputs 432a-432d that present the 4-set values T, B, R and L to the number of inputs 434a-434d of the segment detector 422.

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Please replace the Abstract beginning at page ²⁵~~24~~, line 1 with the following paragraph:

A method for classifying a first video type and a second video type in a digital video signal having a series of frames is disclosed, ~~comprising the steps~~ The method generally includes a first step of (A) reading a first set of parameters defining an active portion of a first of the frames~~7~~. A second step may involve (B) reading a second set of parameters defining an active portion of a second of the frames~~7~~. A third step includes (C) comparing the first set of ~~said~~ the parameters with the second set of parameters to generate a comparison value~~7~~. As such, (D) if the